

| Type | Hits | Search Text | DBs | Time Stamp | Comments | Error Definition | Errors |
|----------|-------|--|--|---------------------|----------|------------------|--------|
| 1 BRS | 1067 | 438/637 and (contact adj holes) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/03/30 08:35 | | | 0 |
| 2 BRS | 4 | (438/637 and (contact adj holes)) and (single adj photomask) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/03/30 08:41 | | | 0 |
| 3 BRS | 47 | (438/637 and (contact adj holes)) and (bit adj line) adj (contact adj holes) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/03/30 09:46 | | | 0 |
| 4 BRS | 2 | ((438/637 and (contact adj holes)) and (bit adj line) adj (contact adj holes)) and (gate adj contact adj hole) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/03/30 09:50 | | | 0 |
| 5 BRS | 4 | ((438/637 and (contact adj holes)) and (bit adj line) adj (contact adj holes)) and (substrate adj contact adj hole) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/03/30 09:50 | | | 0 |
| 6 BRS | 0 | (semiconductor adj chip) and (contact adj hole) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/03/30 13:57 | | | 0 |
| 7 BRS | 32178 | "g" and (contact adj hole) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/03/30 13:58 | | | 0 |
| 8 BRS | 0 | ((semiconductor adj chip)) and (contact adj hole) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/03/30 13:58 | | | 0 |

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|----|------|-------|---|--|---------------------|----------|------------------|--------|
| 9 | BRS | 9 | (semiconductor adj chip) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/03/30 13:58 | | | 0 |
| 10 | BRS | 48458 | semiconductor and (contact adj hole) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/03/30 15:06 | | | 0 |
| 11 | BRS | 293 | (semiconductor and (contact adj hole)) and (gate adj contact adj hole) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/03/30 15:07 | | | 0 |
| 12 | BRS | 868 | (semiconductor and (contact adj hole)) and (bit adj line adj contact adj hole) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/03/30 15:08 | | | 0 |
| 13 | BRS | 2 | ((semiconductor and (contact adj hole)) and (gate adj contact adj hole)) and (bit adj line adj contact adj hole) and (substrate adj contact adj hole) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/03/30 15:08 | | | 0 |
| 14 | BRS | 16 | ((semiconductor and (contact adj hole)) and (gate adj contact adj hole)) and (bit adj line adj contact adj hole) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/03/30 15:25 | | | 0 |